

PATENT
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In the Matter of the Application of:

Christopher N. Brindle, et al.

Serial No.: 10/791,243

Filed: March 2, 2004

For: Methods Of Manufacture For A Low Control Voltage Switch

Examiner: Ngan V. Ngo Group Art Unit: 2818

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Commissioner for Patents
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I hereby certify that this correspondence is being fax filed to the United States Patent and Trademark Office, Fax No. (571) 273-0025 on August 31, 2005.


Paula M. Capriglione

**INFORMATION DISCLOSURE
STATEMENT TRANSMITTAL LETTER**

Sir:

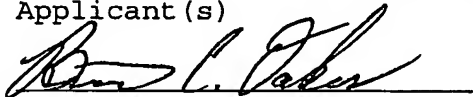
An Information Disclosure Statement is submitted herewith pursuant to 37 CFR §§1.97-1.98. The enclosed statement is being filed within three months of the filing date of a national application, or within three months of the date of entry into the national stage as set forth in 37 CFR §1.491 in an international application, or before the mailing date of a first Office Action on the merits, whichever event occurs last. Additionally, Applicants request that the examiner make the article to Tanaka (cited herein) of record. This document was cited in the parent application 10/391,259 (now U.S. Patent No. 6,730,453) and, as such, no copy has been provided.

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10/791,243

However, in the event a fee is due in connection with this communication, the Commissioner is authorized to charge Deposit Account No. 23-1950.

Respectfully submitted,

Christopher N. Brindle, et al.
Applicant(s)



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BCO/pmc

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
(Rev. 2-32) PATENT AND TRADEMARK OFFICE

ATTY. / DOCKET NO. / SERIAL NO.
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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

APPLICANT
Christopher N. Brindle, et al.

FILING DATE / GROUP
3/2/04 / 2814

U.S. PATENT DOCUMENTS							
EXAM INIT		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FIL. DATE
	AA	4,631,470	12/86	Bingley	323	282	12/85
	AB	4,837,530	6/89	Kondoh	333	81 A	12/87
	AC	4,849,903	7/89	Fletcher et al.	364	482	7/87
	AD	4,890,077	12/89	Sun	333	81 A	3/89
	AE	5,012,123	4/91	Ayasli et al.	307	112	3/89
	AF	5,216,587	6/93	Miyazaki et al.	363	56	4/92
	AG	5,333,104	7/94	Tamura et al.	363	19	5/93
	AH	5,384,501	1/95	Koyama et al.	327	336	5/93
	AI	5,488,552	1/96	Sakamoto et al.	363	21	10/93
	AJ	5,592,353	1/97	Shinohara et al.	361	63	11/94

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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRNS Y/N
	AK	JP 2001 76897	3/01	Japan	H05H	1/46	Abst.
	AL	JP 59 14647	1/84	Japan	H01L	21/82	Abst
	AM	JP 4 6958	1/92	Japan	H04N	3/23	Abst.
	AN						
	AO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	AP		
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	AS		

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U.S. PATENT DOCUMENTS							
EXAM INIT		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FIL. DATE
	AA	5,614,771	3/97	Ahmed et al.	307	113	12/95
	AB	5,656,956	8/97	Ohta et al.	326	120	1/96
	AC	5,675,290	10/97	Tsukahara et al.	330	277	4/96
	AD	5,852,360	12/98	Levinson	323	316	4/97
	AE	5,903,177	5/99	Schwab et al.	327	308	9/96
	AF	5,969,560	10/99	Kohama et al.	327	308	10/97
	AG	5,970,964	10/99	Furuhata et al.	123	644	12/96
	AH	5,973,552	10/99	Allan	327	544	11/96
	AI	6,094,021	7/00	Noro et al.	318	138	10/99
	AJ	6,218,890	4/01	Yamaguchi et al.	327	427	7/99

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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRNS Y/N
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U.S. PATENT DOCUMENTS							
EXAM INIT		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FIL. DATE
	AA	6,246,598	6/01	Tarter et al.	363	56.01	8/00
	AB	6,265,925	7/01	Wong et al.	327	308	9/99
	AC	6,335,577	1/02	Baba	307	28	2/00
	AD	6,445,729	9/02	Kang	375	219	12/98
	AE	6,548,363	4/03	Wu et al.	438	305	4/00
	AF	6,563,366	5/03	Kohama	327	382	10/98
	AG	6,577,171	6/03	Filippo et al.	327	108	11/00
	AH	6,664,842	12/03	Pobanz	327	436	12/01
	AI	6,696,680	2/04	Wada et al.	250	214R	2/03
	AJ	6,704,550	3/04	Kohama et al.	455	114	9/00

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	AK						
	AL						
	AM						
	AN						
	AO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	AP		Tanaka et al. "A 3V MMIC Chip Set for 1.9GHz Mobile Communication Systems." Feb. 16, 1995, International Solid State Circuits Convention. pp. 144-145, 353.
	AR		
	AS		

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